

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. HORIGUCHI, et al.
Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial
No. 10/259,579, filed September 30, 2002
Filed: On even date herewith
For: SEMICONDUCTOR DEVICE HAVING A POWER DOWN
MODE (As Amended)
Art Group of Parent: 2818
Examiner of Parent: H. V. Ho

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 2, 2003

Sir:

Prior to examination, please amend the above-identified application as listed
below and as set forth on the following pages:

Amendments to the Title and Specification

Remarks are included following the amendments.